Modifications for V1.3

- **5V compatible I^{2}C pads**

- **Pipeamp fix (sticky charge)**
  ⇨ simulation with delayed clocks required

- **Daisy chain controller**

- **Power routing (FE, pipeamp, power pads)**

- **Comparator changes**
  ⇨ has to be decoupled from analogue path

- **Overvoltage Problen**
  ⇨ probably related to output driver

- **include DAC probe pads**

  ⇨ perform PSSR measurement